

PlasmaTherm

Recipes for etch selectivity and sidewall verticality

Stevan Djordjevic

Of interest to those patterning Silicon Nitride or Silicon Oxide, NCNC's most recent plasma etch system, The PlasmaTherm, will serve as a new tool in your arsenal. We thank you for your patience as Dan expertly brought the system back to life. Now online and characterized, The PlasmaTherm shows the following strengths and weaknesses:

PlasmaTherm's advantages over it's cousin, the Technics:

- More vertical sidewalls

- Faster Silicon Nitride etch rate

- Greater etch reproducibility (though still subject to change based on system use)

PlasmaTherm's disadvantages to it's cousin, the Technics:

- Significantly more difficult to learn and use

- Slower Silicon and Silicon Oxide etch rate

- No photoresist bulk etching, stripping or ashing allowed

- No 3-5 or similar compounds (eg GaAs, InP, CdTe) allowed

Please forward your questions or training requests to Mike at mnirving@ucdavis.edu

The following three pages of line graphs provide information on the effect of plasma composition and power on the etch rate of several thin films.

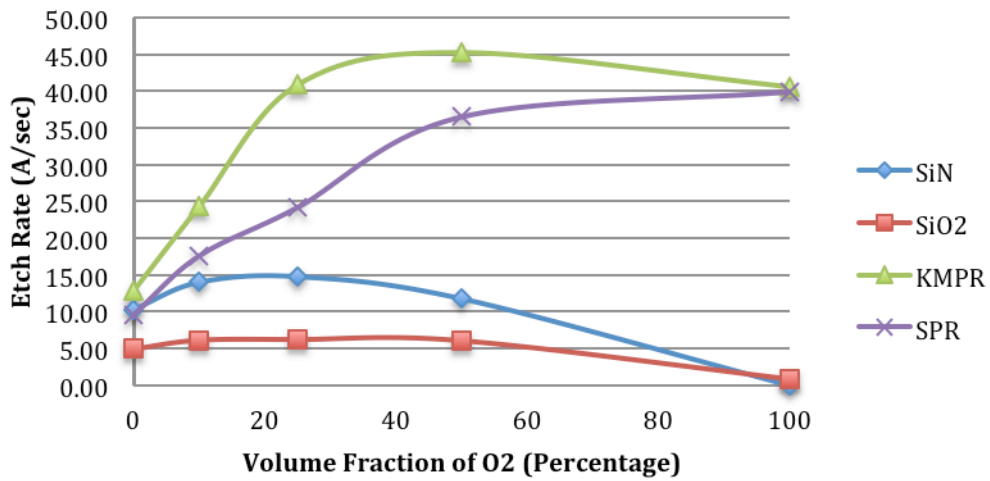
Data on sidewall verticality will be coming soon.

All recipes for pages 2 - 4 were run at 40mTorr pressure and 40sccm total flow rate. Graphs showing the effect of plasma composition were run at 175W. Graphs showing the effect of power were run with a composition of 10:9:1 Ar/CF₄/O₂. A 5 minute plasma clean was run between each etch. All recipes were run on 1cm x 1cm chips of 550um thick Si. Etch rates were determined using a combination of reflectometry and stylus profilometry.

Also, please give thanks to Stevan Djordjevic of the Yoo group who provided documentation of his experiments to optimize the PlasmaTherm's etch verticality and sidewall roughness on stoichiometric Silicon Nitride coated chips, seen on pages 5 and on.

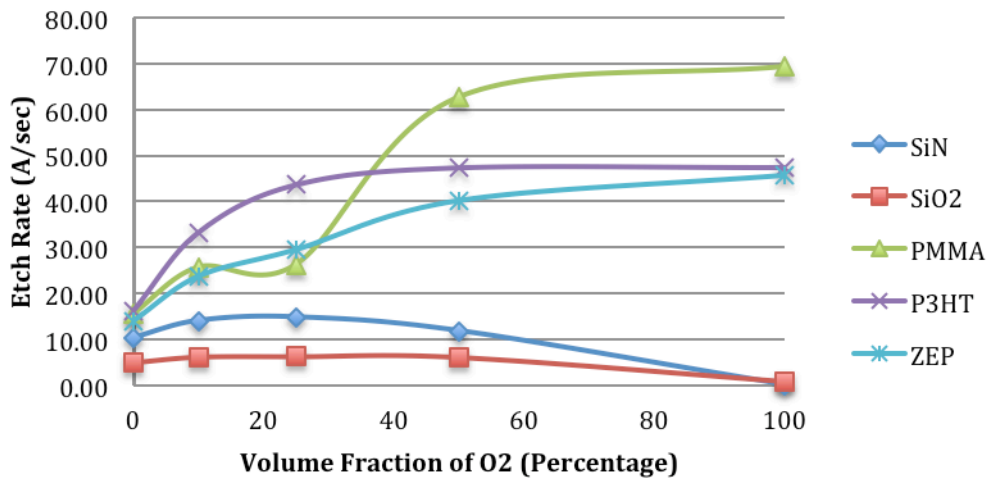
Etch Rate In CF4/O2 Plasma Photoresists

40 sccm, 40mtorr, 175W



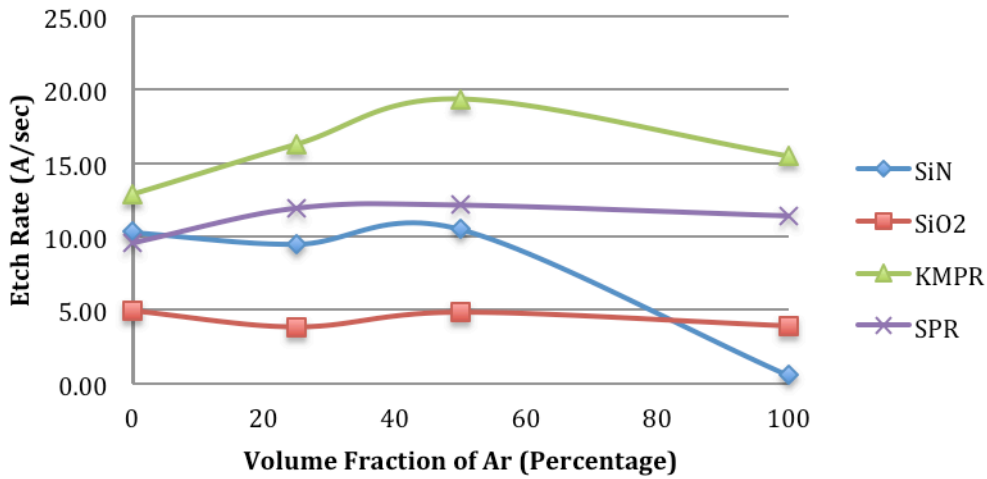
Etch Rate In CF4/O2 Plasma EBL Resists

40 sccm, 40mtorr, 175W

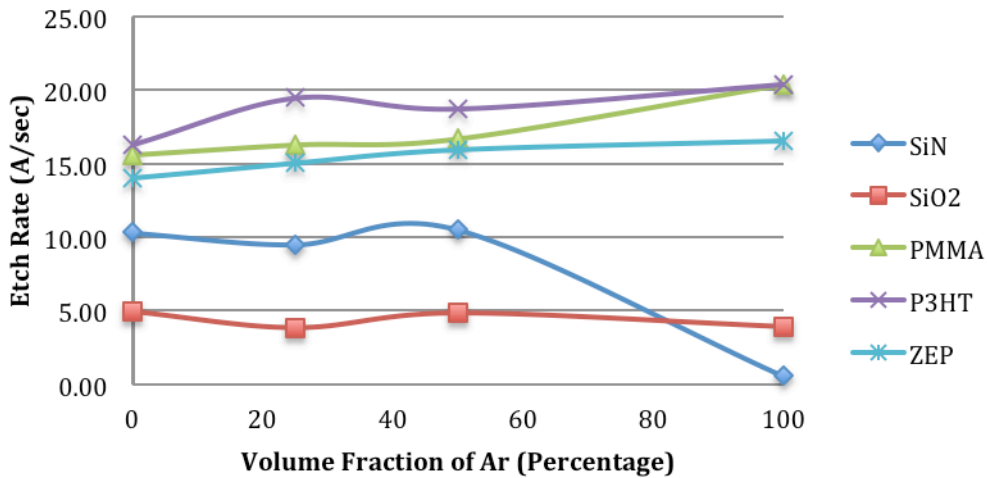


CF4/O2 plasmas find common use in etching Silicon Oxide and Silicon Nitride using a polymeric resist for patterning. Typical recipes utilize 5-15% O2 in the plasma. Though the chart shows data for a pure O2 plasma, please do not ash bulk resists in the PlasmaTherm.

Etch Rate In CF₄/Ar Plasma Photoresists 40 sccm, 40mtorr, 175W

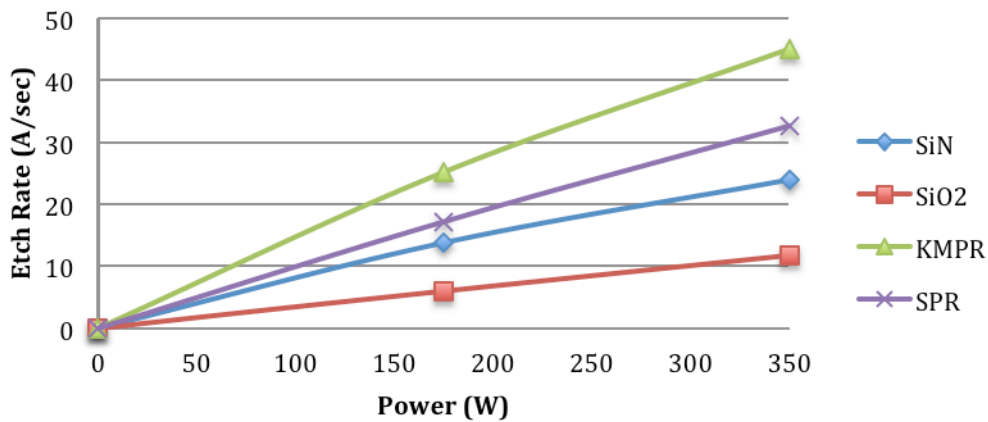


Etch Rate In CF₄/Ar Plasma EBL Resists 40 sccm, 40mtorr, 175W

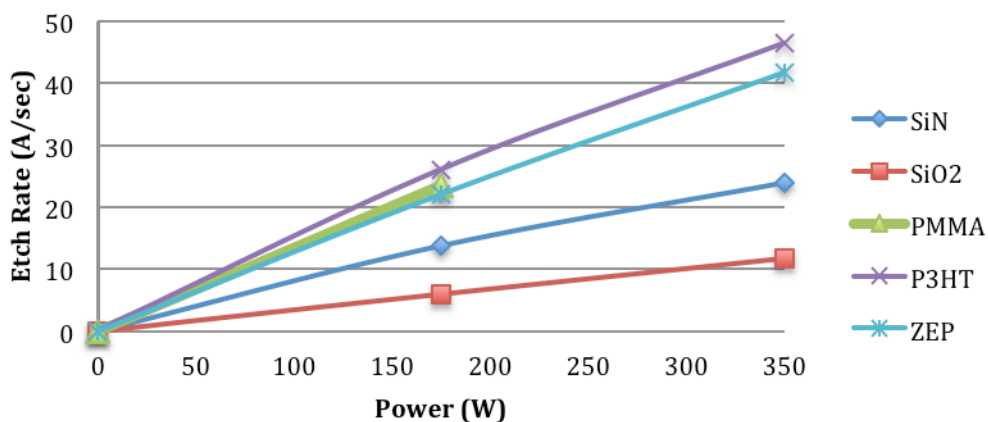


Diluting the plasma with Argon reportedly reduces the amount of grass formed while not reducing resist selectivity as much as Oxygen does. Including as much as 50% Argon did not drastically change etch rates for most materials.

**Etch Rate In 10:9:1 Ar/CF4/O2
Plasma
Photoresists
40 sccm, 40mtorr**



**Etch Rate In 10:9:1 Ar/CF4/O2
Plasma
EBL Resists
40 sccm, 40mtorr**



The PlasmaTherm controls platen bias with RF power. Etch rates show a linear dependence on RF power at 40mtorr.

Materials of interest – measured deposition parameters on dummy silicon wafers

Material	DCS [sccm]	N2O [sccm]	NH3 [sccm]	Temp [C]	Pressure [mTorr]	Dep. time [hrs]	Dep. rate [nm/min]	Uniformity on wafer [%]	Tensile stress [GPa]
SSN	25	0	75	800	300	2	3.34*	1.7	1.10
LSN	100	0	25	835	140	2	3.0	2.4	0.31
HTO	180	180	0	920	400	2	5.1	2.8	- 0.16**

SSN = Stoichiometric silicon nitride LSN = Low stress nitride HTO = High temperature oxide

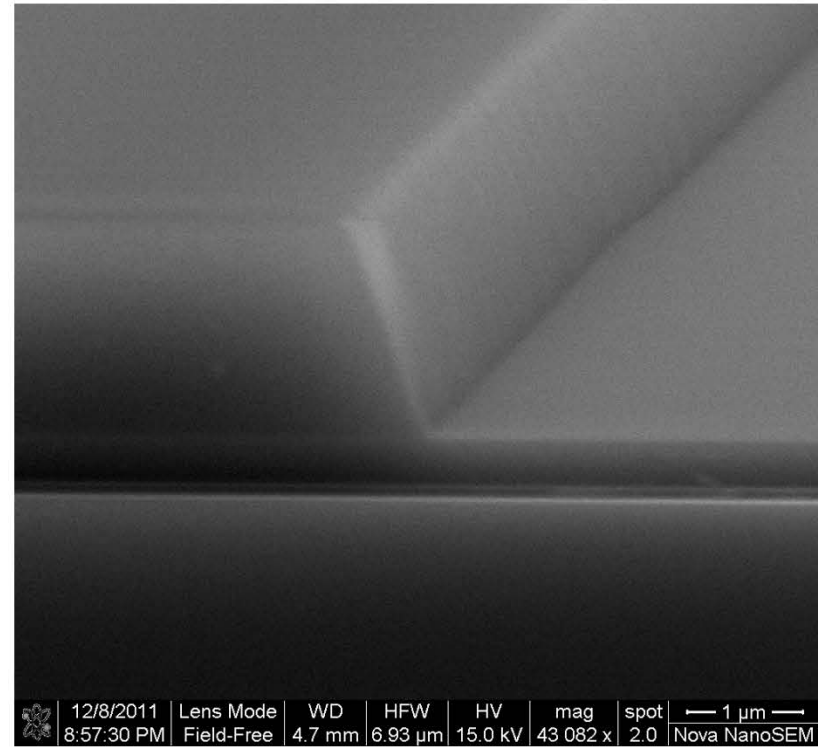
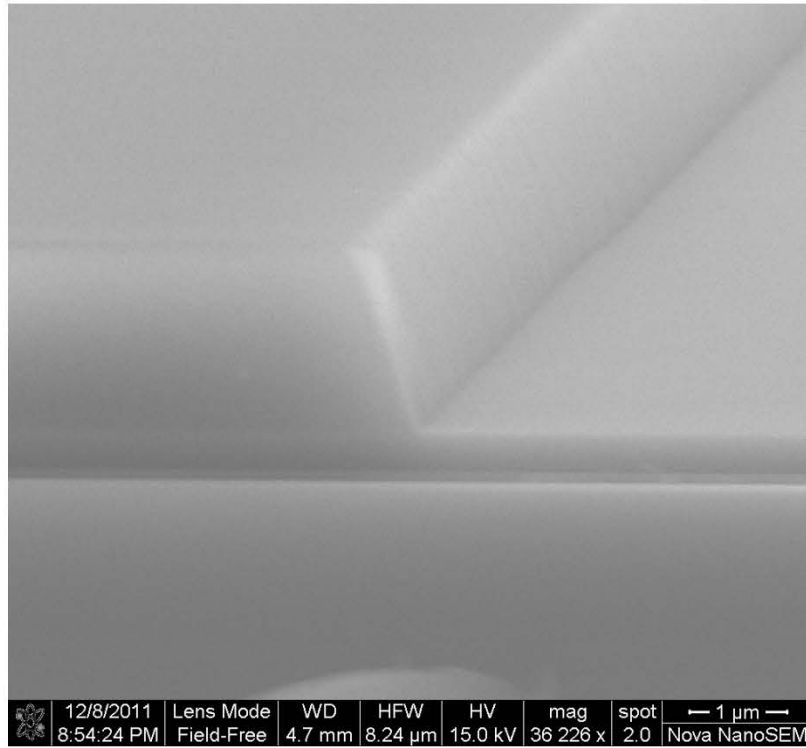
* measurement confirmed by SEM

** compressive stress (stress measured on Tencor Flexus -2320 machine)

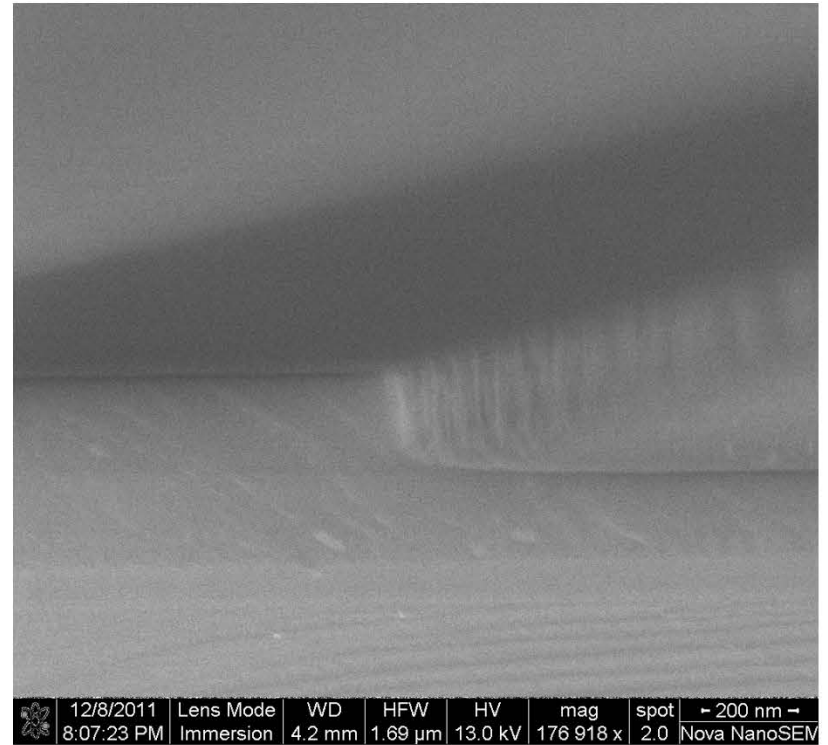
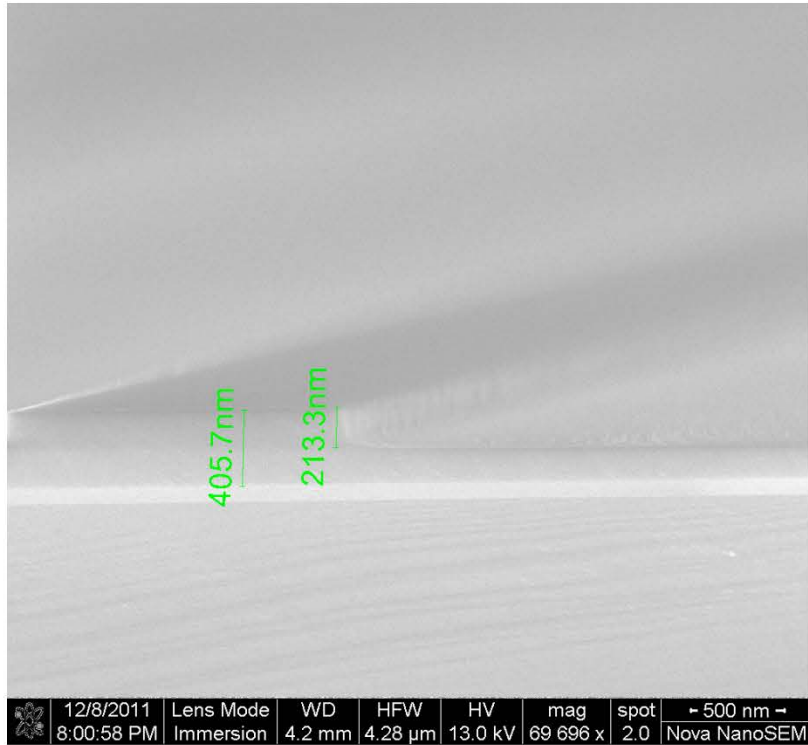
Rec. #	CF4 [sccm]	O2 [sccm]	Ar [sccm]	Press [mTorr]	RF [W]	DC* [W]	Time [s]	Etch rate [nm/min]	SEM
1	40	0	0	40	200	345	240	53.2	Yes
2	40	0	0	20	200	345	240	50.2	Yes
3	40	0	0	40	400	345	240	96.0	Yes
4	40	0	0	10	150		240		No
5	40	0	0	10	100		240		No
6	30	0	10	10	100		240		No

* In the Plasma Therm etch system DC bias can not be controlled independently from the RF power (and vice versa).

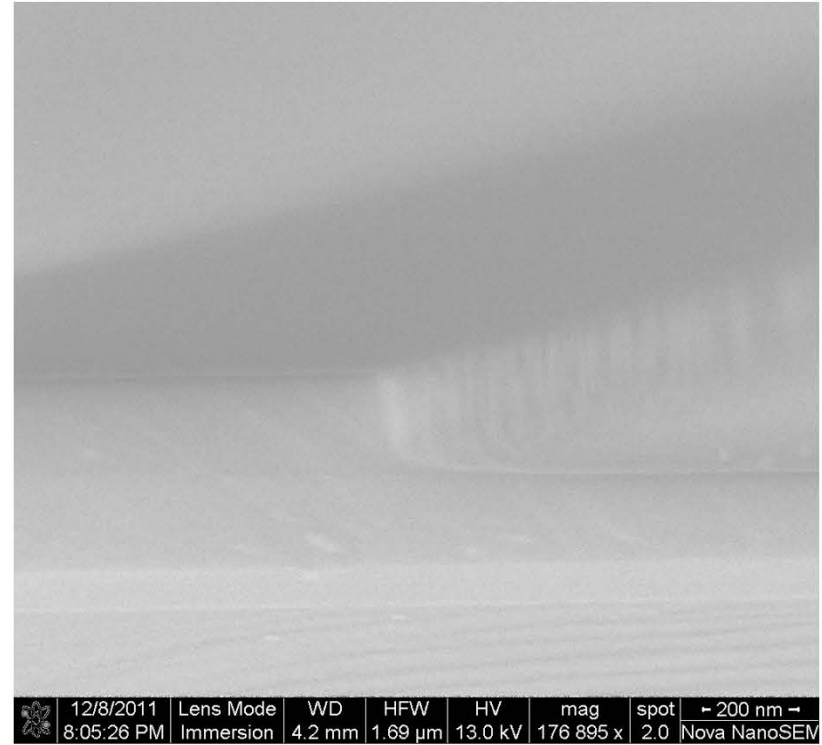
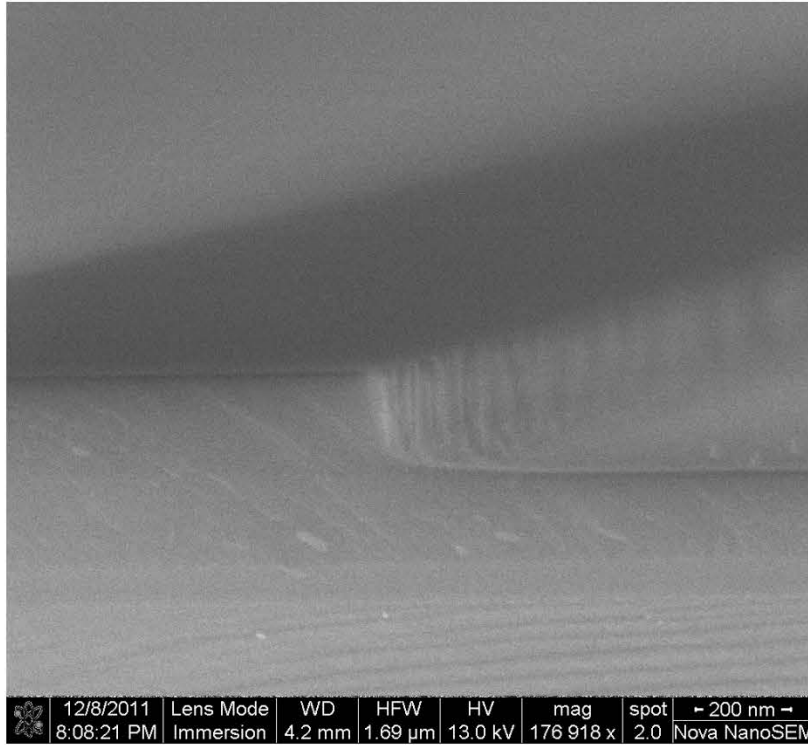
Photoresist pattern (AZ1813)



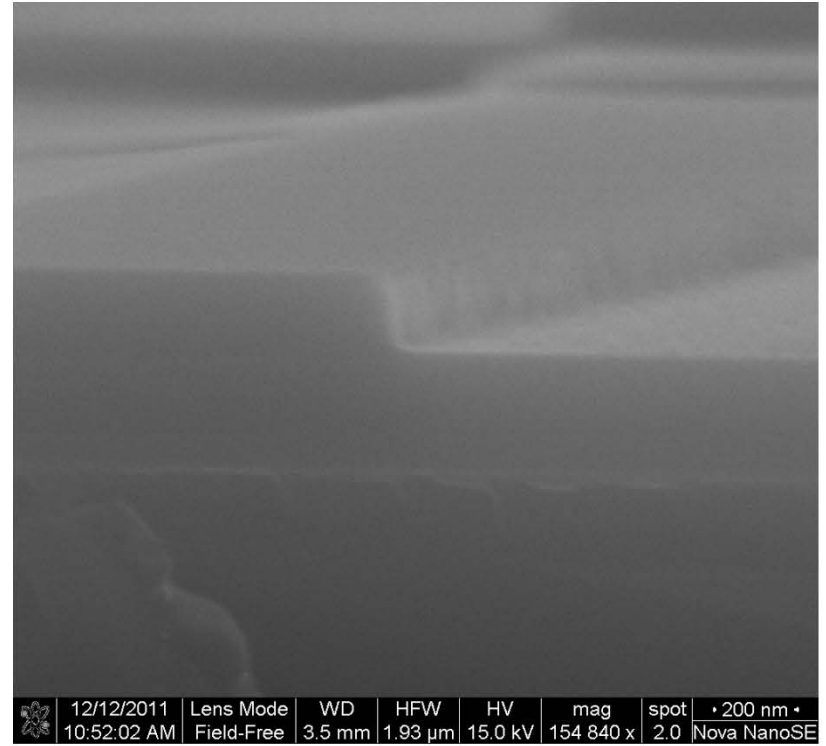
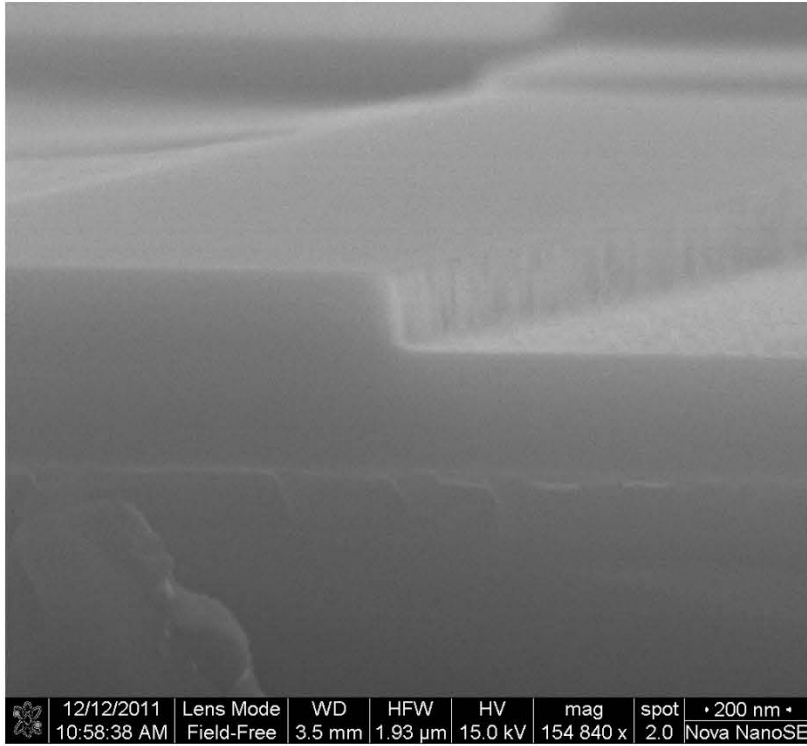
Recipe #1



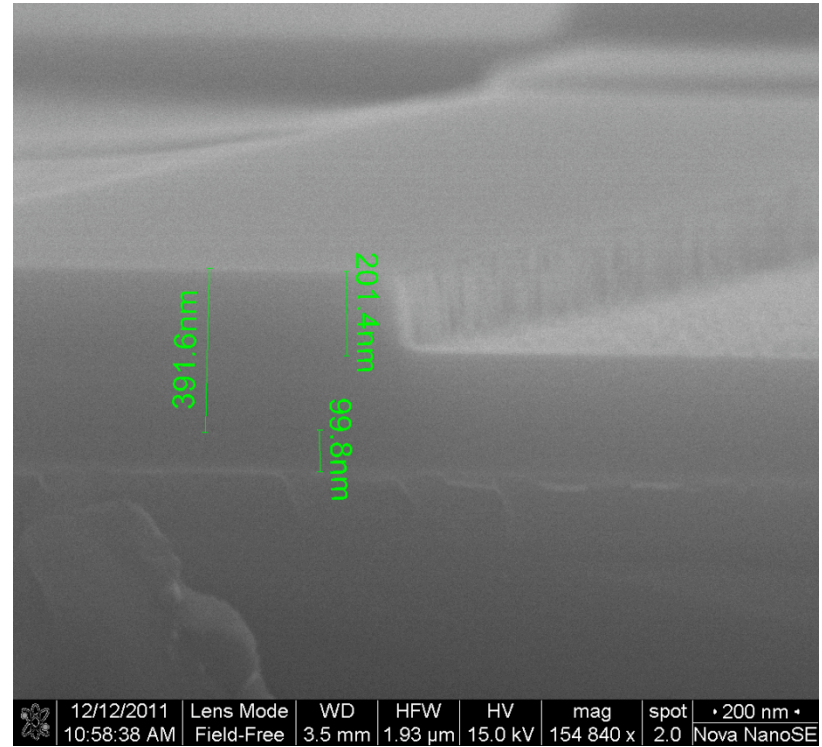
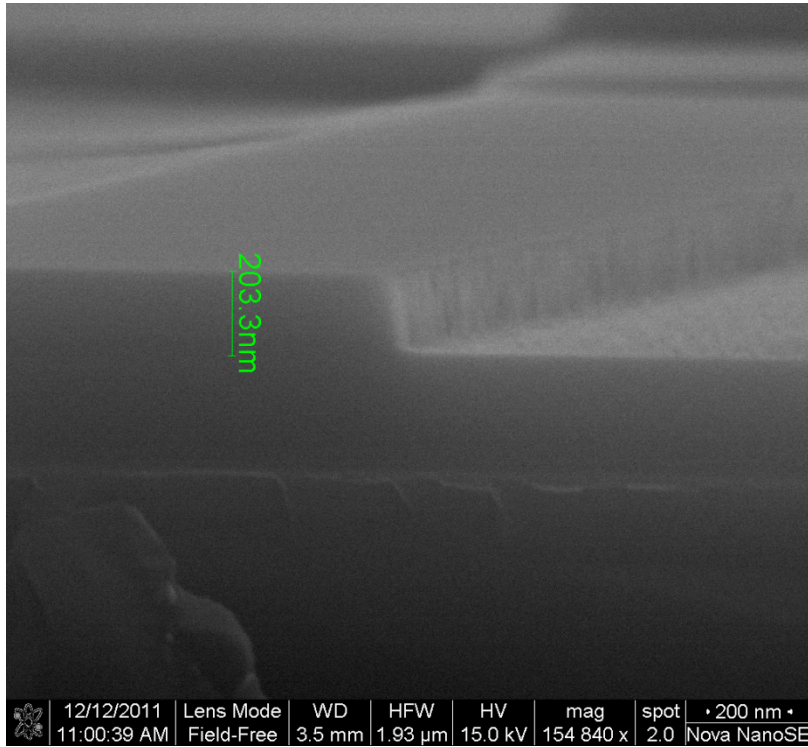
Recipe #1



Recipe #2



Recipe #2



Recipe #3

