

# Schottky barriers: An effective work function model

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The experimental observations of metallurgical interactions between compound semiconductor substrates and metallic or oxide overlayers have stimulated a new model of Fermi level "pinning" at these interfaces. This model assumes the standard Schottky picture of interface band alignment, but that the interface phases involved are not the pure metal or oxide normally assumed by other models. For both III-V and II-VI compounds, the barrier height to gold is found to correlate well with the anion work function, suggesting the interface phases are often anion rich. This correlation holds even for cases in which the "common anion rule" fails, and explains both successes and failures of this earlier model.

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Metal-semiconductor contacts, while crucial to semiconductor devices and studies, are still not well understood. Models relating Schottky barrier heights to metal workfunction, electronegativity, and heats of condensation and reaction with substrate constituents, as well as semiconductor properties such as surface and interface states, heats of formation, polarizability, ionicity, band gap, and defect energy levels can all be found in the recent literature. Some of these models assume the interface to occur abruptly between the two desired phases, while other models require the occurrence of the metallurgical interactions recently observed. The wealth of models available, and the diversity of assumptions they invoke, imply that the fundamentally important aspects of Schottky barrier formation have not yet been established.

In spite of the rich array of various models there are some notable experimental results which remain unexplained. One is the fact that liquid gallium will make a temporary ohmic contact to lightly doped *n*-type GaAs under the conditions in which the native oxide to GaAs is disrupted exposing clean gallium to an oxide free GaAs surface.<sup>1</sup> With time and exposure to air the contact will become rectifying as predicted by previous models. The second and more convincing result is the Okamoto *et al.* study<sup>2</sup> of Schottky barrier heights for the Al-(GaAs-AlAs) interface prepared by molecular beam epitaxy. They find barrier heights, particularly to AlAs, which are significantly different from those predicted by previous models and which are significantly different from those for Au-AlAs.<sup>3</sup> We have reexamined earlier models in light of the recent observations of interface intermixing and propose that the simple Schottky picture of work-function matching—if coupled with mixed phases at the interface—appears to account for a large amount of experimental data and suggests directions for research in controlling Fermi level pinning.

Our model begins with that of Schottky,<sup>4</sup> which assumes an ideal metal-semiconductor interface, i.e., one in which the interface is inert and there are no appreciable surface or induced interface states in the semiconductor. The Schottky barrier height is given by<sup>4</sup>

$$\phi_{bn} = \Phi_M - \chi,$$

$$\phi_{bp} = (E_G/q) + \chi - \Phi_M,$$

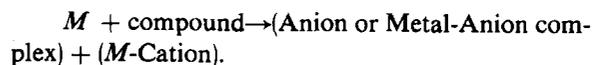
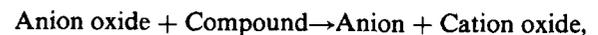
where  $\phi_{bn}$  ( $\phi_{bp}$ ) is the Schottky barrier height to an *n*-type (*p*-type) semiconductor,  $\Phi_M$  is the metal work function,  $q$  is the electron's charge, and  $\chi$  is the electron affinity of the semiconductor. Thus, for the ideal case and for a given semiconductor,  $\phi_b$  should be determined by the metal work function. Unfortunately, this is not the case for GaAs and many other semiconductors.<sup>3</sup>

Our model, called the effective work function model (EWF), suggests that the Fermi level at the surface (or interface) is not fixed by surface states but rather is related to the work functions of microclusters of the one or more interface phases resulting from either oxygen contamination or metal-semiconductor reactions which occur during metalization. The theory requires that when a metal is deposited, or an oxide is formed, there is a region at the interface which contains a mixture of microclusters of different phases, each having its own work function. We should therefore modify the "ideal" surface discussion as follows:

$$\phi_{bn} = \Phi_{\text{eff}} - \chi,$$

where  $\Phi_{\text{eff}}$  is an appropriately weighted average of the work functions of the different interface phases. Thus the measured  $\phi_{bn}$  can depend somewhat on the measurement technique, i.e., *C-V* or *I-V*.

For most of the compounds under discussion, metalization and/or oxidation results in a condition in which  $\Phi_{\text{eff}}$  is due mainly to  $\Phi_{\text{Anion}}$ , the work function of the anion; we suggest that this occurs as a result of one or both of the following reactions:



The condition for driving this reaction to the right and hence generating excess Anion at the interface is that the Gibbs free energy  $\Delta F$  is negative. Such oxide reactions have been examined,<sup>5</sup> and excess group V anions have been experimentally observed when  $\Delta F$  is negative, i.e., for GaAs, InAs, and InSb.<sup>6,7</sup> This has not been observed when  $\Delta F$  is positive, i.e., for GaP.<sup>7</sup> It is interesting to note that for InP,  $\Delta F \approx 0$ ; it has been possible to form metal-oxide semiconductor field-effect transistor (MOSFET) structures using SiO<sub>2</sub>, which exhibit a low interface state density<sup>8</sup> on this

TABLE I. <sup>a</sup> Au Schottky barriers.

Compound	$E_G/q + \chi$	$\phi_{bp}$	$\Phi_{Au} = 5.1-5.5 \text{ eV}^b$	
			$E_G/q + \chi - \phi_{bp}$	$\Phi_{Anion}$
GaP	5.86 <sup>c</sup>	0.96 <sup>j</sup>	4.9	5.0 <sup>r</sup>
InP	5.75 <sup>c</sup>	0.85 <sup>k</sup>	4.9	5.0 <sup>r</sup>
AlAs*	5.6-6.0 <sup>d,e</sup>	0.9 <sup>l</sup> (1.4)** <sup>m</sup>	4.7-5.1 (4.2-4.6)**	5.0 <sup>r</sup> (4.8) <sup>p</sup> ( $\Phi_{Al} = 4.0-4.3^{b,r}$ )
GaAs	5.5 <sup>f</sup>	0.5 <sup>l</sup>	5.0	5.0 <sup>r</sup> (4.8) <sup>p</sup>
InAs	5.3 <sup>f</sup>	0.3-0.5 <sup>n,o</sup>	4.8-5.0	5.0 <sup>r</sup> (4.8) <sup>p</sup>
AlSb* <sup>8</sup>	5.2 <sup>8</sup>	0.54 <sup>l</sup>	4.7	4.8 <sup>r</sup> (4.7) <sup>l</sup>
GaSb	4.76 <sup>f</sup>	0.1 <sup>l</sup>	4.7	4.8 <sup>r</sup> (4.7) <sup>l</sup>
InSb	4.77 <sup>f</sup>	$\approx 0.1^l$	4.8(77 K)	4.8 <sup>r</sup> (4.7) <sup>l</sup>
ZnO	7.92 <sup>h</sup>	2.7 <sup>l</sup>	5.2	7.3 <sup>r</sup>
ZnS	7.5 <sup>h</sup>	1.6 <sup>l</sup>	5.9	5.74 <sup>r</sup>
CdS	7.21 <sup>h</sup>	1.63 <sup>l</sup>	5.58	5.74 <sup>r</sup>
GaS*	6.5 <sup>i</sup>	0.75 <sup>p</sup>	5.75	5.74 <sup>r</sup>
ZnSe	6.76 <sup>h</sup>	1.31 <sup>l</sup>	5.45	5.7 <sup>r</sup>
CdSe	6.65 <sup>h</sup>	1.21 <sup>l</sup>	5.44	5.7 <sup>r</sup>
GaSe*	5.4 <sup>i</sup>	0.5 <sup>p</sup>	4.9	5.7 <sup>r</sup>
ZnTe	5.79 <sup>h</sup>	0.65 <sup>q</sup>	5.14	4.88 <sup>r</sup>
CdTe	5.72 <sup>h</sup>	0.78 <sup>l</sup>	4.94	4.88 <sup>r</sup>
GaTe*	4.95 <sup>i</sup>	0.45 <sup>p</sup>	4.5	4.88 <sup>r</sup>

\*Does not obey common anion rule.

\*\*Al-AlAs barriers.

<sup>a</sup>Band gaps were taken from A. G. Milnes and D. L. Feucht, *Heterojunctions and Metal-Semiconductor Junctions* (Academic, New York, 1972), p. 8.

<sup>b</sup>Reference 14.

<sup>c</sup>J. Van Laar, A. Huijser, and T. L. Van Rooy, *J. Vac. Sci. Technol.* **14**, 894 (1977).

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<sup>j</sup>B. L. Smith and M. Abbott, *Solid-State Electron.* **15**, 361 (1972).

<sup>k</sup>B. L. Smith, *J. Phys. D* **6**, 1358 (1973).

<sup>l</sup>C. A. Mead, *Solid State Electron.* **9**, 1023 (1966).

<sup>m</sup>Reference 2.

<sup>n</sup>K. Kajiyama, Y. Mizushima, and S. Sakata, *Appl. Phys. Lett.* **23**, 458 (1973).

<sup>o</sup>J. N. Walpole and K. W. Nill, *J. Appl. Phys.* **42**, 5609 (1971).

<sup>p</sup>S. Kurtin and C. A. Mead, *J. Phys. Chem. Solids* **30**, 2007 (1969).

<sup>q</sup>W. D. Baker and A. G. Milnes, *J. Appl. Phys.* **43**, 5152 (1972).

<sup>r</sup>K. W. Frese, Jr., *J. Vac. Sci. Technol.* **16**, 1042 (1979).

<sup>s</sup>As on native oxide of GaAs, J. L. Freeouf and J. M. Woodall (unpublished).

<sup>t</sup>J. L. Freeouf, M. Aono, F. J. Himpsel, and D. E. Eastman, *J. Vac. Sci. Technol.* (to be published).

material. This is consistent with our model that would predict either no or very little excess free phosphorus at the interface. A GaP MOSFET structure with low interface-state densities would be predicted, since no free P is expected at this interface. It should also be noted that for GaAs it is well known that MOSFET structures have notoriously high "interface-state densities" ( $10^{13}$ - $10^{14} \text{ cm}^{-2}$ ) and that excess arsenic is usually observed at the interface.<sup>9</sup> Again this is consistent with the model, since the  $\phi_{bn}$  expected for the As-GaAs interface is about 0.8 eV (the usually observed barrier height for most metal depositions as well). Since workers have reported a large density of mid-gap states for MOSFET GaAs structures, the model would ascribe these "states" to arsenic clusters at the interface which act as Schottky barrier contacts with  $\phi_{bn} \approx 0.8 \text{ eV}$  embedded in an oxide matrix.

Excess anions can also be generated by reaction of metals with the substrate. For example, it is known that Au deposited on GaAs and GaP results in excess Ga in the Au

film.<sup>10</sup> Also preliminary phase diagram data<sup>11</sup> show that an arsenic phase is expected at equilibrium for Au-GaAs and Au-InSb. Thus a knowledge of both oxide and reactive metal chemistry should enable accurate predictions of the transport properties of metal-semiconductor devices (including Schottky barrier heights).

The current status of this model<sup>12</sup> is shown in Table I, which lists the experimentally derived values of  $\phi_{bp}$  and  $E_G/q + \chi - \phi_{bp}$  for Au/III-V and Au/II-V contacts. There are three points to note in this table. First, the Schottky model ( $E_G/q + \chi - \phi_{bp} = \Phi_{Au} \equiv 5.1-5.5 \text{ eV}$ ) is not obeyed. Second, the EWF model agrees well, as expected, for these data by assuming  $\Phi_{eff}$  to be dominated by  $\Phi_{Anion}$ , i.e.,  $\Phi_{Anion} = E_G/q + \chi - \phi_{bp}$ . Third, the common anion rule<sup>13</sup> is not obeyed for AlAs and AlSb. We believe that the common anion rule followed more directly from the anion than initially suggested; in fact, we believe that this rule followed from the formation of microclusters of anions

at the interface which dominated the Fermi level position determinations cited. The common anion rule asserts that  $\phi_{bp}$  depends only upon the semiconductor anion. Since, in our model,  $\phi_{bp} = ((E_G/q) + \chi) - \Phi_{\text{Anion}}$ , a common anion would lead to a constant  $\phi_{bp}$  only for a constant  $E_G/q + \chi$ ; Table I shows that those cases following the common anion rule also obey that constraint.

The EWF model also explains such departures from "normal" behavior as the Al-AlAs result,<sup>2</sup> also shown in Table I. For the Al-AlAs case, the metalization was performed in an ultrahigh vacuum molecular beam epitaxy system, where the AlAs surface was very clean, and subsequently annealed. Under these conditions, excess As should react with Al rather than forming microclusters of As. Thus, it is expected that  $\Phi_{\text{eff}}$  should be dominated by  $\Phi_{\text{Al}} = 4.0-4.3$  eV. We believe that this explanation is correct, since  $\chi + \phi_{bn} \approx 4.2-4.6$  for this case, which is much closer to  $\Phi_{\text{Al}}$  than to  $\Phi_{\text{As}}$ . Similarly, the Ga-GaAs ohmic contact mentioned earlier can be explained since  $\Phi_{\text{Ga}} = 4.36$  eV (Ref. 14) and  $\phi_{bn} = 0-0.3$  (for ohmic behavior);  $\chi_{\text{GaAs}} + \phi_{bn} = 4.1-4.4 \approx \Phi_{\text{Ga}}$ .

The electrical behavior of most covalent semiconductor interfaces is dominated by the apparent pinning of the Fermi energy level at the interface. We are proposing a model of this behavior which assumes work function matching and (typically) mixed phase behavior at the interface; "pinning"

normally observed is shown to follow naturally from microclusters of anions at the interface, which are expected from chemical arguments and observed in some recent experiments.

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<sup>11</sup>R. S. Williams (private communication).

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